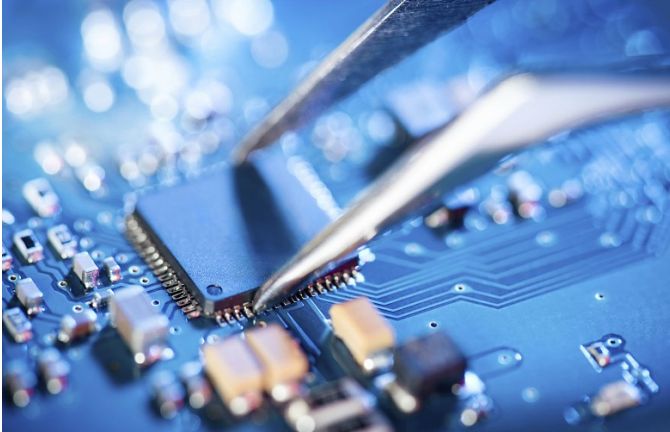


Customer

A global, leading provider of standard and custom integrated circuits for Bluetooth, DECT Wireless and VoIP solutions.



Customer Objectives

- > Implementation and verification of a digital design as part of the development of a next generation ultra low Bluetooth module.


AimValley Solution


Implement a digital design in RTL based on a MATLAB model provided by the customer. The RTL and the verification environment were made in VHDL. The design was verified by means of simulation. The digital design was also evaluated by developing C-code in a bare metal environment design, using an FPGA development board. Development of ARP drivers and preparation of automated testing. Lastly all verification results were documented.


Key Technologies


- > RTL
- > VHDL
- > MATLAB programming language
- > Simulations
- > Low Level Driver bare-metal development

Results and Added Value

 **Efficient**
Successful first-time-right approach.

 **Partnership**
Joint development with customer teams in Netherlands, UK and India. AimValley worked part-time on customer location.

 **Successful**
Full project on-time delivery.

 **Innovation**
Multi-level verification and validation enabled ASIC first-time-right quality by using simulation in RTL code, C-software code on CPU and test on FPGA board.