

# Project: Riffelhorn FPGA and API



## AimValley Solution

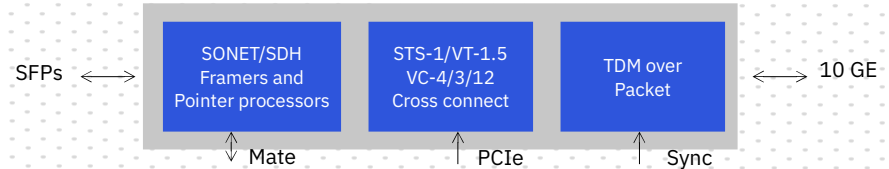
- SW development – PCIe driver API
- FPGA design – TDM matrix and TDM over Packet
- SONET/SDH line interfaces, 10 GE packet backplane
- Line and path protection, VT-1.5 UPSR, Sync ref
- Business model: Design Service, Time & Material

## Key technologies:

- AimValley IP for SONET/SDH and TDMoP

## Customer Objectives

- Lower cost replacement for existing TDM CEM solution
- In-field replaceable unit
- Feature parity or better



## Results & Added Value

Efficiency	Reuse of AimValley TDM and packet IP to reduce time to market and development costs
Successful	Multiple releases with incremental features
Partnership	API integration in customer host SW FPGA design in close cooperation with customer
Innovative	Replace 3 ASICs with 1 FPGA (Xilinx KU15P), enabling cost, size and power reduction